

UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ping-Sheng TSENG Docket No: 16503-0023
Serial No: 09/591,683 Group Art Unit: 2763
Filed: June 9, 2000 Examiner: Not yet assigned
For: **EMULATION SYSTEM WITH MULTIPLE ASYNCHRONOUS CLOCKS**

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Commissioner for Patents
Washington, D.C. 20231

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Technology Center 2100

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Dear Sir:

Please address all further correspondence in the above-identified application to:

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Respectfully submitted,

Date: August 3, 2001


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CERTIFICATE OF MAILING (37 CFR 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited on August 3, 2001, with the U.S. Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

Date: August 3, 2001


Yvette Yurralde-Owen